7

Assignee: Intel Corporation

IN THE SPECIFICATION

Please amend the following paragraphs of the application that are idenified by page and line number of the submitted application as well as by paragraph number for the corresponding published application US 2005/0013251A1.

Page 8, line 27 – page 9, line 6; paragraph [0029]

Figure 4 illustrates an exemplary scoreboard memory 400. The scoreboard memory 400 includes an index 410 associated with a flow or combination of flows, a status 420 indicating the flow control status of the index, and a valid bit 430 indicating whether the index 410 is valid or not. The index 410 may be the same as the address 210 200 of the flow control messages. For example, a flow control message having an ingress port 01, egress port 10 and priority 1, may have an index of 01101 if the index was the same as the address. Alternatively, a mapping table may be utilized to map the address 210 200 to the applicable index 410. As previously discussed, flow control messages may be broadcast. For example, if the flow control message is to be broadcast to all the ingress ports the flow control message will either contain no ingress port ID or will contain an ingress port ID that is associated with a broadcast flow control message.

Page 10, lines 23 – 28; paragraph [0035]

If the index does not have a valid entry (530 No), the logging, merging, and replacing unit 330 will validate the index and mark the status (570). It should be noted that the reason there is no valid entry could be because there is no status data for that index or that the valid bit is not set. This could be because no flow control messages were received for that particular index or that the last flow control message associated with that index was already processed and the status data was erased and/or the valid bit was deactivated.